

## 6NR, 6NV CHANNEL MODULES (Nx64)

The fractional T1/E1 (6NR/6NV) Module provides data circuits, or channels, over which you can transmit and receive data at bandwidths from 64kbps to 1536kbps (N = 1 to 24). The desired bandwidth and clock trigger points are set through the FCS program. There are no DIP switches or other hardware settings. The 6NR/6NV module may be supplied with either a RS-449 (6NR) or V.35 (6NV) electrical interface.

Operating as a synchronous pipeline, the 6NR/6NV channel does not process the data passing through it and provides the input/output clock information. Since the data is not processed, there are no concerns about data protocols or formats. The data clock rate adjusts automatically with the selection of N (N = 1 to 24).

Individual interfaces provide the channel connection of RS-449 or V.35. The module's interface is one DB-25 female connector operating as a DCE device.

### Application

This module can be used wherever synchronous bandwidth greater than, or equal to, a single DS0 is required. This is typically referred to as fractional T1 or E1.

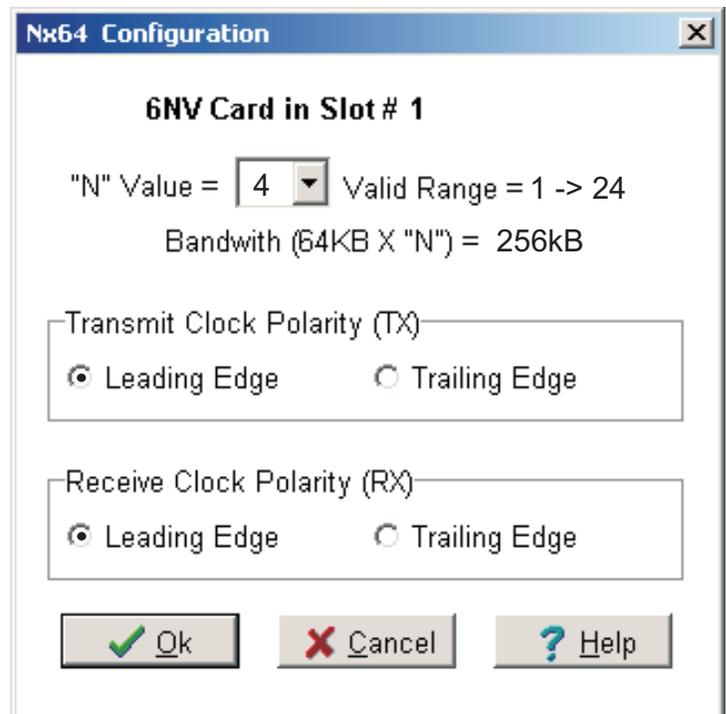
Typical devices you can connect to the module include:

- Current differential protective relaying pilot channel
- Ethernet LAN through externally connected router or switch
- Video surveillance

### Specifications

Module:	1 slot width*
Data Rate:	64k to 1536k (1 to 24 timeslots)
Elec. Interface:	V.35 or RS-449 (6NV or 6NR)
Connection:	25 pin female D-shell (DCE)
Clock:	Leading or Trailing Edge
Indications:	Module Status TX Data RX Data

\* When setting 'N' for values greater than 2, it is necessary to leave empty slots to the right of the module when installed in FOCUS. One spare slot for every 2 values of N greater than N=2. (i.e. 1 spare slot for N=3 or 4, 2 spare slots for N=5 or 6, etc.)



**Nx64 Configuration**

**6NV Card in Slot # 1**

"N" Value =  Valid Range = 1 -> 24  
Bandwith (64KB X "N") = 256kB

Transmit Clock Polarity (TX)  
 Leading Edge  Trailing Edge

Receive Clock Polarity (RX)  
 Leading Edge  Trailing Edge